



# ADS 3000+



## ■ Features

- Compliant with VSI-H, a standard interface for VLBI monitoring
- Two channels, each supporting a maximum of 2.048 GHz sampling
- Supports overwriting of embedded FPGA circuits (enables addition of a digital filter, etc.)
- The FPGA is made by Xilinx company, Two LX110s are embedded
- Possible conversion to 4.096 Gsps × 1 or 1.024 Gsps × 4 (optional)

## ■ Specifications

Sampling clock	2.048GHz
Data input	2 channels, ±250 mVp-p (50Ω)
Reference clock input	Reference clock input
Reference pulse	1 PPS input/output
Data output	VSI-H compliant LVDS output connector: MDR80
Serial port	RS232C D-sub 9-pin male connector × 1
Network	100Base-Tx ×1
Power supply	AC 100 V to AC 230 V
Dimensions (W × H × D)	2 U (480 × 88 × 430 mm)

### Cosmo Research Corp

3-148-5 Miyaharacyo, Kitaku  
Saitama-city Saitama 331-0812 Japan  
TEL (81)48-653-8101 FAX(81)48-667-1129  
[HTTP://www.cosmoresearch.co.jp](http://www.cosmoresearch.co.jp)

### Sales Representative